

U.S. UTILITY Patent Application

SCANNED <i>GR</i>	O.I.P.E. <i>GR</i>	PATENT DATE
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APPLICATION NO. 09/932053	CONT/PRIOR F	CLASS 257 438	SUBCLASS 455	ART UNIT 2815 2823	EXAMINER H. Lee
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APPLICANTS

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Semiconductor package and fabricating method thereof

THE

PTO-2040
12/89[illegible]

DRAWINGS		CLAIMS ALLOWED	
Sketch Ortg.	Pos. Drwg.	Total Claims	Print Claim for O.G.
(Applicant Examiner) (Date) (Patent Examiner) (Date) (Legal Instruments Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
		Amount Due	Date Paid
		ISSUE BATCH NUMBER	

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